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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/774,535	02/10/2004	Jae-Sung Lee	61610115US	3220

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EXAMINER

AL NAZER, LEITH A

ART UNIT PAPER NUMBER

2821

DATE MAILED: 04/19/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

51

Office Action Summary	Application No. 10/774,535	Applicant(s) LEE ET AL.	
	Examiner Leith A. Al-Nazer	Art Unit 2821	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 29 March 2006.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-15 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1,3,7-9,11 and 15 is/are rejected.
- 7) ☒ Claim(s) 2,4-6,10 and 12-14 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 10 February 2004 and 14 October 2005 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1, 3, 7-9, 11, and 15 are rejected under 35 U.S.C. 102(b) as being anticipated by U.S. Patent No. 5,910,792 to Hansen et al.

With respect to claim 1, Hansen teaches an image display, comprising: a display panel including a plurality of pixels arranged in a matrix pattern (figure 4), a plurality of first electrodes individually formed corresponding to the pixels, a second electrode formed in common with the first electrodes (figures 3 and 4), a plurality of light emitting elements provided between the first electrode and the second electrode and including a light emitting layer (304), and a plurality of transistors provided corresponding to the pixels and connected between the first electrodes and a power supply voltage line for controlling the current supply to the EL elements (figures 5-7); a scan driver (420a-c) for sequentially selecting respective pixel lines; a data driver (440) for applying an RGB display signal corresponding to a pixel line of the display panel each time the pixel line is selected; and a display controller (figures 5-7) for using a current value fed back from the second electrode (512; figure 5; column 6, line 64 – column 7, line 18) of the display panel and externally input RGB data (column 8, lines 44-58) to correct a white gray level

of the RGB data and generate RGB display data, and for providing the generated RGB display data to the data driver (440), wherein the display controller (figures 5-7) determines an amount of emitted light on the corresponding screen according to the fed back current to generate a brightness control reference signal corresponding to the amount of emitted light, and controls the white gray level of the RGB data according to the brightness control reference signal to control the brightness of the display panel (figures 4-7; column 5, lines 36-66; column 6, line 64 – column 7, line 18; column 8, lines 44-58).

With respect to claim 3, Hansen teaches the current fed back from the display panel being a summation of currents flowing to the second electrode from the first electrodes of the respective pixels (figure 5; column 6, line 1 – column 7, line 25).

With respect to claim 7, Hansen teaches an image display, comprising: a display panel including a plurality of pixels arranged in a matrix pattern (figure 4), a plurality of first electrodes individually formed corresponding to the pixels, a plurality of second electrodes commonly formed for a plurality of groups defined by defining the first electrodes as the groups (figures 3 and 4), a plurality of light emitting elements provided between the first electrode and the second electrode and including a light emitting layer (304; figure 3), and a plurality of transistors provided corresponding to the pixels and connected between the first electrodes and a power supply voltage line for controlling the current supply to the EL elements (figures 5-7); a scan driver (420a-c) for sequentially selecting respective pixel lines; a data driver (440) for applying an RGB display signal corresponding to a pixel line of the display panel each time the pixel line

Art Unit: 2821

is selected; and a display controller (figures 5-7) for using a current value fed back from at least one second electrode (512; figure 5; column 6, line 64 – column 7, line 18) of the display panel and externally input RGB data (column 8, lines 44-58) to correct a white gray level of the RGB data and to generate RGB display data, and for providing the generated RGB display data to the data driver (440), wherein the display controller determines an amount of emitted light on the corresponding screen according to the fed back current to generate a brightness control reference signal corresponding to the amount of emitted light, and controls the white gray level of the RGB data according to the brightness control reference signal to control the brightness of the display panel (figures 4-7; column 5, lines 36-66; column 6, line 64 – column 7, line 18; column 8, lines 44-58).

With respect to claim 8, Hansen teaches a method for driving an image display, comprising: sequentially selecting respective pixel lines (figure 4); applying an RGB display signal corresponding to a pixel line of the display panel each time the pixel line is selected (figure 4; column 8, lines 44-58); and using a current value fed back from the second electrode of the display panel and externally input RGB data to correct a white gray level of the RGB data and generate RGB display data, and to provide the generated RGB display data to a data driver (figures 4-7; column 5, lines 36-66; column 6, line 64 – column 7, line 18; column 8, lines 44-58).

With respect to claim 9, Hansen teaches an image display, comprising: a display panel including a plurality of pixels arranged in a matrix pattern (figure 4); a scan driver (420a-c) for sequentially selecting respective pixel lines; a data driver (440) for applying

Art Unit: 2821

an RGB display signal corresponding to a pixel line of the display panel each time the pixel line is selected; and a display controller (figures 5-7) for using a current value fed back from an electrode of the display panel (512; column 6, line 64 – column 7, line 18) and externally input RGB data (column 8, lines 44-58) to correct a white gray level of the RGB data and generate RGB display data, and for providing the generated RGB display data to the data driver, wherein the display controller determines an amount of emitted light on the corresponding screen according to the fed back current to generate a brightness control reference signal corresponding to the amount of emitted light, and controls the white gray level of the RGB data according to the brightness control reference signal to control the brightness of the display panel (figures 4-7; column 5, lines 36-66; column 6, line 64 – column 7, line 18; column 8, lines 44-58).

With respect to claim 11, Hansen teaches the current fed back from the electrode of the display panel being a summation of currents flowing to a second electrode from one or more first electrodes that each correspond to a respective pixel (figure 5; column 6, line 1 – column 7, line 25).

With respect to claim 15, Hansen teaches a method for driving an image display, comprising: sequentially selecting respective pixel lines (figure 4); applying an RGB display signal corresponding to a pixel line of the display panel each time the pixel line is selected (figure 4; column 8, lines 44-58); and using a current value fed back from an electrode of a display panel and externally input RGB data to correct a white gray level of the RGB data and generate RGB display data, and to provide the generated RGB

Art Unit: 2821

display data to a data driver (figures 4-7; column 5, lines 36-66; column 6, line 64 – column 7, line 18; column 8, lines 44-58).

Allowable Subject Matter

3. Claims 2, 4-6, 10, and 12-14 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

4. The following is a statement of reasons for the indication of allowable subject matter:

With respect to dependent claims 2 and 10, the prior art of record fails to teach or suggest the display controller comprising the combination of a current voltage converter, an operational controller, a data voltage ratio controller, and a voltage amplifier.

Response to Arguments

5. Applicant's arguments with respect to claims 1, 3, 7-9, 11, and 15 have been considered but are moot in view of the new ground(s) of rejection.

Citation of Pertinent References

6. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. The following references further show the state of the art with respect to image display controllers:

- a. U.S. Patent No. 6,388,649 to Tanaka et al.
- b. U.S. Patent No. 5,889,503 to Kikuchi et al.

Communication Information

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Leith A. Al-Nazer whose telephone number is 571-272-1938. The examiner can normally be reached on Monday-Friday, 7:30-4:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy Callahan can be reached on 571-272-1740. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

LA



**THUY V. TRAN
PRIMARY EXAMINER**